

7th International Conference on VLSI, Communication, and Signal Processing (VCAS 2024)

Department of Electronics & Communication Engineering

Motilal Nehru National Institute of Technology Allahabad, Prayagraj, India

October 25 - 26, 2024

Schedule

Date & Time	9 AM - 10 AM	10 AM - 11 AM	11 AM- 11:30 AM	11:30 AM - 1 PM	1 PM- 2:30 PM	2:30 PM - 4 PM	4 PM - 5:00 PM
DAY 1 October 25, 2024 Friday	Registration ISH	<u>Inauguration</u> ISH	High Tea ISH	<u>Keynote Talk #1</u> Prof. Satybrata Jit (IIT BHU) ISH	Lunch ISH	Physical Paper Presentations Session #1 (LHC 2) Online Paper Presentations <u>Session #2</u> <u>Session #3</u>	Tea ISH
DAY 2 October 26, 2024 Saturday	Registration ECED	<u>Keynote Talk #2</u> Prof. R K Gangwar (IIT Dhanbad) CH-ECE	Tea ECED	Technical Workshop #1 VLSI Lab, ECED Technical Workshop #2 Signal Processing Lab, ECED Technical Workshop #3 Microwave Lab, ECED	Lunch ECED	<u>Valedictory Function</u> (2:30 PM – 3:30 PM) CH-ECE	

ISH: Institute Seminar Hall

CH-ECE: Conference Hall, ECED