



MOTILAL NEHRU NATIONAL INSTITUTE OF TECHNOLOGY ALLAHABAD, PRAYAGRAJ

मोतीलाल नेहरू राष्ट्रीय प्रौद्योगिकी संस्थान इलाहाबाद, प्रयागराज



Estd. 1961

मोतीलाल नेहरू राष्ट्रीय प्रौद्योगिकी संस्थान
इलाहाबाद

Motilal Nehru National Institute of Technology
Allahabad

VCAS | 2024

OCTOBER 25-27

The 7th International Conference
on VLSI, Communication and Signal Processing

Call for paper on
“VLSI, Communication and Signal Processing”

Selected & presented papers from the conference will be published by
Springer as a proceedings book volume indexed in **Scopus** database.



Springer



Scopus®

Organised By:

The Department of Electronics and Communication Engineering,
Motilal Nehru National Institute of Technology Allahabad,
Prayagraj, Uttar Pradesh, India, 211004

About the Conference:

The Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology Allahabad, proudly announces the hosting of 7th International Conference on VLSI, Communication and Signal Processing (VCAS 2024). The conference aims to provide a platform for academicians, individuals working in the industry, as well as students to showcase their research innovations and get recognition. VCAS 2024 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Communications and Signal Processing. In a quest to regard all classes of research-oriented audience from across the globe, the three-day conference (VCAS 2024) is proposed to be held from October 25, 2024 to October 27, 2024. The scope of the conference embraces all the latest research trends in Electronics and Communication Engineering.

Organizing Committee

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Organizing Joint Secretary:	Dr. Anand Sharma, ECED, MNNIT Allahabad, India

Important Dates

Paper Submission Starts	May 1, 2024
Paper Submission Deadline	July 31, 2024
Notification of Acceptance & Registration Starts	September 2, 2024
Registration Ends	October 06, 2024
Camera Ready Paper Submission Deadline	October 14, 2024
Conference Date	October 25-27, 2024

Registration Fee

CATEGORY	INDIAN DELEGATES	FOREIGN DELEGATES
Student	INR 10,620	USD 236
Faculty	INR 14,160	USD 295
Industry	INR 23,600	USD 354

(Including GST 18%)

Publications



Selected & presented papers from the conference are likely to be published by Springer as a proceedings book volume, satisfying the following criterion:

- Paper should be registered and presented in the conference.
- Paper should be plagiarism free as per Springer norms.

MICROSOFT CMT LINK FOR PAPER SUBMISSION:

<https://cmt3.research.microsoft.com/VCAS2024>

Tracks

VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit, Low-Power Design, Packaging & Interconnect, 2D material-based Devices, MEMS & NEMS design and Technology, Devices Modeling and Process simulation, Thin Film, Ultra-High-Speed Transistors, HEMT/HBT, Reconfigurable Architecture Design, Power Devices and Reliability, Artificial Intelligence (Process & Device), Internet of Things (Process & Device), Emerging Technology.

Communication

Communication Theory and Applications, Optical Networks and Systems Microwave Communications, Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks, IOT and Sensor Networks, Antenna Design for Communication Systems and Networks, Vehicular Communications and Networks, Wireless Modeling/Algorithms and Simulation, Cognitive Radio and Spectrum Management, Adaptive Communication Systems and Networks, Intelligent Protocols and Designing for Communication Systems and Networks, Computer Communication.

Signal Processing

Signal Processing: Theory, Methods and Applications, Signal Processing Systems and Architectures, Signal Processing Algorithms, Image/Video and Multidimensional Signal processing, Multimedia Signal Processing, Biomedical Signal Processing and Bioinformatics, Signal Processing for Communication and Networking, Signal Processing for Machine Learning and IoT, Gnostic Signal Processing, Coding and Compression, Signal Processing for Smart systems, Sparse Signal Processing, Signal processing for VLSI.

* The Topics of interest include but are not limited to the above-mentioned Tracks.



VCAS 2024

Contact Us

- ✉ vcas.eced@mnnit.ac.in
- ☎ (+91) 532 227 1475
- 📍 Organizing Secretary, VCAS 2024
ECED, MNNIT Allahabad,
Prayagraj, Uttar Pradesh, India - 211004
- 🌐 <http://mnnit.ac.in/vcas2024/>