

Book Chapter:

1. P. Ghosal, **M. Sarkar**, and P. Chatterjee, "A New Paradigm towards Performance Centric Computation beyond CMOS: DNA Computing", in *Nano-CMOS and Post-CMOS Electronics: Vol 2. Circuits and Design, The Institute of Engineering and Technology (IET)*, Chapter 12, pp. 379-408, doi: 10.1049/PBCS030E_ch12

Journals:

1. **M. Sarkar**, P. Ghosal and S.P. Mohanty, "Exploring the Feasibility of a DNA Computer: Design of an ALU Using Sticker-Based DNA Model", in *IEEE Transactions on NanoBioscience*, vol. 16, no. 6, pp. 383-399, Sept. 2017, doi: 10.1109/TNB.2017.2726682
2. **M. Sarkar**, P. Ghosal and S.P. Mohanty, "Minimal reversible circuit synthesis on a DNA computer", *Natural Computing, Springer*, vol. 16, pp. 463-472, 2017, doi: <https://doi.org/10.1007/s11047-016-9553-6>
3. Biswas T, Kuila P, Ray AK, and **Sarkar M**, "Gravitational search algorithm based novel workflow scheduling for heterogeneous computing systems", *Simulation Modelling Practice and Theory*, vol. 96, 2019. doi: <https://doi.org/10.1016/j.simpat.2019.101932>

Conference Proceedings:

1. **M. Sarkar**, P. Ghosal and S. P. Mohanty, "Reversible circuit synthesis using ACO and SA based Quine-McCluskey method", in *2013 IEEE 56th International Midwest Symposium on Circuits and Systems (MWSCAS)*, Columbus, OH, USA, 2013, pp. 416-419, doi: 10.1109/MWSCAS.2013.6674674.
2. **M. Sarkar** and P. Ghosal, "Mathematics Using DNA: Performing GCD and LCM on a DNA Computer", in *2016 IEEE International Symposium on Nanoelectronic and Information Systems (iNIS)*, Gwalior, India, 2016, pp. 240-243, doi: 10.1109/iNIS.2016.062.
3. **M. Sarkar** and P. Ghosal, "Performing Mathematics Using DNA: Complex Number Arithmetic Using Sticker Model", in *2017 IEEE Computer Society Annual Symposium on VLSI (ISVLSI)*, Bochum, 2017, pp. 568-573, doi: 10.1109/ISVLSI.2017.105.
4. **M. Sarkar** and P. Ghosal, "Post CMOS Computing Beyond Si: DNA Computer as Future Alternative", in *2016 IEEE International Symposium on Nanoelectronic and Information Systems (iNIS)*, Gwalior, India, 2016, pp. 129-133, doi: 10.1109/iNIS.2016.039.
5. **M. Sarkar** and P. Ghosal, "Implementing Data Structure Using DNA: An Alternative in Post CMOS Computing" in *2015 IEEE Computer Society Annual Symposium on VLSI (ISVLSI)*, Montpellier, France, 2015, pp. 345-349, doi: 10.1109/ISVLSI.2015.106.
6. P. Chatterjee, **M. Sarkar** and P. Ghosal, "Computing in Ribosomes: Implementing Sequential Circuits Using mRNA-Ribosome System", in *2016 IEEE International Symposium on Nanoelectronic and Information Systems (iNIS)*, Gwalior, India, 2016, pp. 230-235, doi: 10.1109/iNIS.2016.060.
7. P. Chatterjee, **M. Sarkar** and P. Ghosal, "Computing in Ribosomes: Performing Boolean Logic Using mRNA-Ribosome System", in *2016 IEEE Computer Society Annual Symposium on VLSI (ISVLSI)*, Pittsburgh, PA, USA, 2016, pp. 260-265, doi: 10.1109/ISVLSI.2016.128.
8. S. Shakhari, P. Ghosal and **M. Sarkar**, "A Provably Good Method to Generate Good DNA Sequences", in *2016 IEEE International Symposium on Nanoelectronic and Information Systems (iNIS)*, Gwalior, India, 2016, pp. 134-138, doi: 10.1109/iNIS.2016.040.

Technical Reports:

1. P. Ghosal, and **M. Sarkar**, "Beyond Silicon: Is DNA Computer Going to be the Future?", in *VLSI Circuits and Systems Letter*, Volume 1, Issue 1, April 2015.
2. P. Ghosal, P. Chatterjee, and **M. Sarkar**, "Bringing out a Natural Computer (Ribosome) from within a Cell: A Next-Gen Alternative?" in *VLSI Circuits and Systems Letter*, Volume 2, Issue 1, April 2016.