

PUBLICATIONS IN INTERNATIONAL JOURNALS

1)	Varun Mishra, Yogesh Kumar Verma, Lucky Agarwal, Santosh Kumar Gupta, "Temperature Impact on Device Characteristics of Charge Plasma Based Tunnel FET with Si_{0.5}Ge_{0.5} Source" , <i>Engineering Research Express</i> , , Published By IOP. (Scopus , IF = 0.5)
2)	Devarshi Shukla, Santosh Kumar Gupta, Vijaya Bhadauria, Rajeev Tripathi, "An Inverter Amplifier with Resistive Feedback Current Mirror Gilbert Mixer" , <i>International Journal of Electronics</i> , , Published By Taylor & Francis. (SCI , IF = 1.336)
3)	S. K. Gupta, S. Baishya, "Design Considerations of electrically induced source/drain junctions Sillion-On-Insulator MOSFETs for the reduced short channel and hot electron effects" , <i>IACSIT International Journal of Computer and Electrical Engineering (IJCEE)</i> , vol.3, Issue 6, pp.869-872, 12/2011, Published By IACSIT.
4)	S. K. Gupta, Gaurab Gunjan Pathak, Debajit Das, Chandan Sharma, "Design and Simulation of a Two Stage OPAMP Using DG MOSFETs for Low Power and Low Voltage Applications" , <i>Design and Simulation of a Two Stage OPAMP Using DG MOSFETs for Low Power and Low Voltage Applications</i> , vol.1, Issue 3, pp.60-63, 12/2011,
5)	S. K. Gupta, Kaushik Guha, S. Baishya, "Simulation and Modeling of Double Material Double Gate Surround Gate (DMDG-SG) MOSFETs" , <i>Journal of Programmable Devices, Circuits, and Systems</i> , vol.12, Issue 1, pp.19-27, 12/2012, Published By ICGST.
6)	S. K. Gupta, S. Baishya, "Modeling of Built-in Potential Variations of Cylindrical Surrounding Gate (CSG) MOSFETs" , <i>International Journal of VLSI Design and Communication Systems (VLSIC)</i> , vol.3, Issue 5, pp.67-77, 10/2012, Published By AIRCC Publishing Corporation.
7)	S. K. Gupta, S. Baishya, "A TCAD Simulation Study of Cylindrical Gate All Around MOSFETs" , <i>Global Journal of Researches in Engineering-F (GJRE)</i> , vol.12, Issue 9, pp.21-24, 08/2012, Published By Global Journals Inc. (US).
8)	S. K. Gupta, S. Baishya, "Modeling and Simulation of Triple Metal Cylindrical Surround Gate MOSFETs for reduced Short Channel Effects" , <i>International Journal of Soft Computing and Engineering (IJSCE)</i> , vol.2, Issue 2, pp.214-221, 05/2012, Published By Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd. (BEIESP). (IF = 4.96)
9)	S. K. Gupta, Achinta Baidya, S. Baishya, "Simulation and Analysis of Gate Engineered Triple Material Double Gate MOSFETs for Diminished Short Channel Effects" , <i>International Journal of Advanced Science and Technology</i> , vol.38, pp.15-24, 01/2012, Published By SERSC.
10)	Santosh Kumar Gupta, Srimanta Baishya, "Subthreshold Modeling of Cylindrical Surrounding Gate MOSFETs Including the Fringing Field Effects" , <i>IOP Journal of Semiconductors (Earlier Chinese Journal of Semiconductors)</i> , vol.34, Issue 7, pp.074001-6, 07/2013, (Scopus)
11)	S. K. Gupta, S. Baishya, "TCAD Analysis of a novel Junctionless Triple Metal Cylindrical Surround Gate (JLTM CSG) MOSFET" , <i>Research Journal of Recent Sciences</i> , vol.2, Issue 6, pp.1-9, 06/2013, Published By ISCA.

12)	Santosh Kumar Gupta, Srimanta Baishya, " Analog and RF Performance Evaluation of Dual Metal Double Gate High-k Stack (DMDG-HKS) MOSFETs ", <i>Journal of Nano- and Electronic Physics</i> , vol.5, Issue 3, pp.03008-8p, 03/2013, (Scopus)
13)	S. K. Gupta, S. Baishya, " Analog and RF Performance of Junction Less Dual Metal Cylindrical Surround Gate (JL-DM CSG) MOSFETs ", <i>Research Journal of Recent Sciences</i> , vol.2, Issue 1, pp.44-52, 01/2013, Published By ISCA.
14)	Santosh Kumar Gupta, Srimanta Baishya, " On the analog and RF performance of Junctionless Single Metal Gate (JLSM CSG) MOSFETs ", <i>Simulation: Transactions of the Society for Modeling and Simulation International (Sage Journals)</i> , vol.90, Issue 10, pp.1119-1128, 10/2014, (SCI , IF = 0.818)
15)	Santosh Kumar Gupta, " Threshold Voltage Model of Junctionless Cylindrical Surrounding Gate MOSFETs including Fringing Field Effects ", <i>Superlattices and Microstructures (Elsevier)</i> , vol.88, pp.188-197, 12/2015, (SCI , IF = 2.097)
16)	Santosh Kumar Gupta, Girija Nandan Jaiswal, " Study of Nitrogen Terminated doped zigzag GNR FET Exhibiting Negative Differential Resistance ", <i>Superlattices and Microstructures (Elsevier)</i> , vol.86, pp.355-362, 10/2015, (SCI , IF = 2.097)
17)	Santosh Kumar Gupta, Srimanta Baishya, " Performance Analysis of a novel Junction less Electrically Induced Source/Drain regions Cylindrical Surround Gate (JLEJ CSG) MOSFET ", <i>Journal of The Institution of Engineers (INDIA): Series B (Springer)</i> , vol.96, Issue 3, pp.211-216, 07/2015,
18)	Akash Singh Rawat, Santosh Kumar Gupta, " Potential modeling and performance analysis of junction-less quadruple gate MOSFETs for analog and RF applications ", <i>Microelectronics Journal (Elsevier)</i> , vol.66, pp.89-102, 06/2017, (SCI , IF = 1.163)
19)	Manoj Kumar Yadav, Santosh Kumar Gupta, Sanjeev Rai, Avinash C. Pandey, " AI Embedded MgO Barrier MTJ: A first principle Study for Application in Fast and Compact STT-MRAMs ", <i>Superlattices and Microstructures (Elsevier)</i> , vol.103, pp.314-324, 03/2017, (SCI , IF = 2.097)
20)	Santosh Kumar Gupta, Mihir Kumar P. Shah, " Quasi-3D modeling of surface potential and threshold voltage of Triple Metal Quadruple Gate MOSFETs ", <i>Superlattices and Microstructures (Elsevier)</i> , vol.101, pp.455-468, 01/2017, (SCI , IF = 2.097)
21)	Varun Mishra, Yogesh Kumar Verma, Prateek Kishor Verma, Santosh Kumar Gupta, " EMA Based Modeling of Surface Potential and Drain Current of Dual Material Gate All Around TFETs ", <i>Journal of Computational Electronics</i> , vol.17, Issue 4, pp.1596–160, 12/2018, Published By Springer. (SCI , IF = 1.637)
22)	Abhinav Gupta, Anamika Singh, Santosh K. Gupta, Sanjeev Rai, " Potential Modeling of Oxide Engineered Doping-Less Dual-Material-Double-Gate Si-Ge MOSFET and Its Application ", <i>Journal of Nanoelectronics and Optoelectronics</i> , vol.13, Issue 8, pp.1115–112, 08/2018, Published By American Scientific Publisher. (SCI , IF = 1.069)
23)	Mohammad Kaifi, Santosh Kumar Gupta, " Simulation of Perovskite based Solar Cell and Photodetector using SCAPS Software ", <i>International Journal of Engineering Research and</i>

	<i>Technology (IJERT)</i> , vol.12, Issue 10, pp.1778-1786, 10/2019, Published By International Research Publication House (IRPH). (Scopus , IF = 0.145)
24)	Yogesh Kumar Verma, Varun Mishra, Santosh Kumar Gupta, " Electrical Characterization of AlGaN/GaN Quadruple Gate Heterostructure Field Effect Transistor for Analog Applications ", <i>Journal of Nanoelectronics and Optoelectronics</i> , vol.14, Issue 10, pp.1358–136, 10/2019, Published By American Scientific Publishers. (SCI , IF = 1.069)
25)	Santosh Kumar Gupta, Satyaveer Kumar, " Analytical Modeling of a Triple Material Double Gate TFET with Hetero-Dielectric Gate Stack ", <i>Silicon</i> , vol.11, Issue 3, pp.1355–136, 06/2019, Published By Springer. (SCI , IF = 1.210)
26)	Yogesh Kumar Verma, Santosh Kumar Gupta, Varun Mishra, Prateek Kishor Verma, " Analytical Modeling and Electrical Characterization of ZnO based HEMTs ", <i>International Journal of Electronics</i> , vol.106, Issue 5, pp.707–720, 05/2019, Published By Taylor and Francis. (SCI , IF = 1.070)
27)	Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, " Investigation of Localized Charges on Linearity and Distortion Performance of Ferroelectric Dual Material Gate All Around TFETs ", <i>Journal of Nano-and Electronic Physics</i> , vol.11, Issue 4, pp.04014-1-04, 04/2019, Published By Sumy State University (Sumy, Ukraine). (Scopus , IF = 0.201)
28)	Santosh Kumar Gupta, Rupesh Shukla, " Bandgap Engineered novel g-C3N4/G/h-BN Heterostructure for Electronic Applications ", <i>Journal of Semiconductors</i> , vol.40, Issue 3, pp.032801-5, 03/2019, Published By IOP Science. (Scopus , IF = 0.28)
29)	Vadthiya Narendar, Santosh K. Gupta, Shikhar Saxena, " First Principle Study of Doped Graphene for FET Applications ", <i>Silicon</i> , vol.11, Issue 1, pp.277–286, 02/2019, Published By Springer. (SCI , IF = 0.829)
30)	Santosh Kumar Gupta, Akash Singh Rawat, Yogesh Kumar Verma, Varun Mishra, " Linearity Distortion Analysis of Junctionless Quadruple Gate MOSFETs for Analog Applications ", <i>Silicon</i> , vol.11, Issue 1, pp.257–265, 02/2019, Published By Springer. (SCI , IF = 0.829)
31)	Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, " Investigation of Localized Charges and Temperature Effect on Device Performance of Ferroelectric Dual Material Gate All Around TFETs ", <i>Journal of Nanoelectronics and Optoelectronics</i> , vol.14, Issue 2, pp.161-168, 02/2019, Published By American Scientific Publishers. (SCI , IF = 1.069)
32)	Manoj Kumar Yadav, Santosh Kumar Gupta, " A Comparative First Principles Study of Quantum Well States in MgO Barrier MTJs for STT-RAMs ", <i>Microelectronics Journal</i> , vol.105, Issue 11, pp.104909, 11/2020, Published By Elsevier. (SCI , IF = 1.405)
33)	Prateek Kishor Verma, Yogesh Kumar Verma, Varun Mishra, Santosh Kumar Gupta, " A charge-plasma-based dual-metal-gate recessed-source/drain dopingless junctionless transistor with enhanced analog and RF performance ", <i>Journal of Computational Electronics</i> , vol.19, Issue 3, pp.1085-1099, 09/2020, Published By Springer. (SCI , IF = 1.637)
34)	Yogesh Kumar Verma, Santosh Kumar Gupta, " Center Potential Based Analysis of Si and III-V Gate

	<i>all around Field Effect Transistors (GAA-FETs)</i> ", <i>Silicon</i> , vol.33, pp.https://do, 07/2020, Published By Springer. (SCI , IF = 1.210)
35)	Yogesh Kumar Verma, Varun Mishra, Santosh Kumar Gupta, " Performance Enhancement of AlGaN/GaN HEMT by Optimization of Device Parameters considering Nano-meter barrier layer thickness for Analog Applications ", <i>International Journal of Nanoscience</i> , vol.19, Issue 6, pp.2050011, 06/2020, Published By World Scientific. (Scopus , IF = 0.197)
36)	Varun Mishra, Yogesh K. Verma, Santosh K. Gupta, " Surface Potential Based Analysis of Ferroelectric Dual Material Gate All Around (FE-DMGAA) TFETs ", <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , vol.33, Issue 4, pp.e2726, 06/2020, Published By Wiley. (SCI , IF = 0.795)
37)	Yogesh Kumar Verma, Varun Mishra, Santosh Kumar Gupta, " A Physics based analytical model for MgZnO/ZnO HEMT ", <i>Journal of Circuits, Systems, and Computers</i> , vol.29, Issue 1, pp.2050009, 01/2020, Published By World Scientific Publishing Company. (SCI , IF = 0.595)
38)	Yogesh Kumar Verma, Varun Mishra, Santosh Kumar Gupta, " Linearity Distortion Analysis of III-V and Si Quadruple Gate Field Effect Transistor (QG-FET) for Analog Applications ", <i>Journal of Nanoelectronics and Optoelectronics</i> , vol.15, Issue 1, pp.1-18, 01/2020, Published By American Scientific Publishers. (SCI , IF = 1.069)
39)	Afreen Haque, Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, " Investigation of Novel Low Bandgap Source Material for Hetero-dielectric GAA-TFET with Enhanced Performance ", <i>Silicon</i> , 11/2021, Published By Springer. (SCI , IF = 2.67)
40)	Abhishek Kumar, Santosh Kumar Gupta, Vijaya Bhadauria, " Low-power and low glitch area current steering DAC ", <i>Engineering Science and Technology, an International Journal</i> , pp. https://doi.org/10.1016/j.jestch.2021.06.015, 07/2021, Published By Elsevier. (SCI , IF = 4.36)
41)	Mamidi Nagaraju, Santosh Kumar Gupta, Vijaya Bhadauria, " Design and Implementation of Parallel Bypass Bin Processing for CABAC Encoder ", <i>Advances in Electrical and Electronic Engineering</i> , 06/2021, Published By VSB - Technical University of Ostrava and University of Zilina Faculty of Electrical Engineering. (Scopus SJR: 0.225), DOI: 10.15598/aeec.v19i3.4010
42)	Prateek Kishor Verma, Santosh Kumar Gupta, " An Improved Analog/RF and Linearity Performances with Small-Signal Parameter Extraction of Virtually Doped Recessed Source/Drain Dopingless Junctionless Transistor for Radio-Frequency Applications ", <i>Silicon</i> , vol.13, Issue 5, pp.1519–153, 05/2021, Published By Springer. (SCI , IF = 1.210)
43)	Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, Vikas Rathi, " A SiGe-Source Doping-Less Double-Gate Tunnel FET: Design and Analysis Based on Charge Plasma Technique with Enhanced Performance ", <i>Silicon</i> , 03/2021, Published By Springer. (SCI , IF = 1.499)
44)	Yogesh Kumar Verma, Varun Mishra, and Santosh Kumar Gupta, " Analog/RF and linearity distortion analysis of novel MgZnO/CdZnO Quadruple-Gate Heterostructure Field Effect Transistor (QG-HFET) ", <i>Silicon</i> , vol.13, Issue 1, pp.91–107, 01/2021, Published By Springer. (SCI , IF = 1.21)
45)	Prateek Kishor Verma, Santosh Kumar Gupta, " Proposal of Charge Plasma based Recessed

	<i>Source/Drain Dopingless Junctionless Transistor and its Linearity Distortion Analysis for Circuit Applications</i> ", <i>Silicon</i> , vol.13, Issue 1, pp.37–64 , 01/2021, Published By Springer. (SCI , IF = 1.210)
46)	Mamidi Nagaraju, Santosh Kumar Gupta, Vijaya Bhadauria, <i>“High-throughput, area-efficient hardware architecture of CABAC-Binarization for UHD applications”</i> , <i>Microelectronics Journal</i> , online 16 March 2022, 105425, Volume 123, May 2022, 105425, (https://doi.org/10.1016/j.mejo.2022.105425), Published by Elsevier, (SCI, IF=1.605)
47)	Manoj Kumar Yadav, Santosh Kumar Gupta, <i>“FeAl/MgO/FeAl MTJ with enhanced TMR and low resistance area product for MRAM: A first principle study”</i> , <i>Micro and Nanostructures</i> , corrected proof online 26 March 2022, Published by Elsevier, Volume 165, May 2022, 207192 (SCI, IF=2.658) (https://doi.org/10.1016/j.micrna.2022.207192)
48)	Sushmita Jaiswal, Santosh Kumar Gupta, <i>“Digital Performance Analysis of Double Gate MOSFET by Incorporating Core Insulator Architecture”</i> , <i>Silicon</i> , Published By Springer, Accepted: 9 March 2022. Online: 30 March 2022, (SCI , IF = 1.210) (https://doi.org/10.1007/s12633-022-01811-7)
49)	Kumar A.; Gupta S.K.; Bhadauria V., <i>“Low-power and low glitch area current steering DAC”</i> , <i>Engineering Science and Technology, an International Journal</i> , online 10 July 2021, Volume 29 No. 5, Year 2022, 101035, (DOI: https://doi.org/10.1016/j.jestch.2021.06.015) [IF=4.360 (2020)]
50)	Verma Y.K.;Mishra V.;Adhikari M.S.;Buddhi D.;Gupta S.K., <i>“Comparative Analysis of Different Figures of Merit for AlGaIn/GaN and Si Surrounding-Gate Field Effect Transistors (SG-FETs)”</i> , <i>Silicon</i> , Published 06 April 2021, Volume 14 No. 4, Year 2022, Pages 3027-3036 (SCI , IF = 1.210) (https://doi.org/10.1007/s12633-021-01099-z)
51)	Abhishek Kumar, Santosh Kumar Gupta, & Vijaya Bhadauria, <i>“Design of IF-RF based heterodyne transmitter using current steering DAC with 5.4 GHz spur-free bandwidth”</i> , <i>IETE Journal of Research (TIJR)</i> , Published online: 03 May 2022 (https://doi.org/10.1080/03772063.2022.2069168) [IF=2.333 (2020)]
52)	Abhishek Kumar, Santosh Kumar Gupta, Vijaya Bhadauria, <i>“A 12-bit SC3 partially segmented current steering DAC with improved SFDR and bandwidth”</i> , <i>International Journal of Circuit Theory and Applications</i> , Wiley, First published: 04 May 2022 , Volume 50, Issue 8, pp. 2941-2959 (https://doi.org/10.1002/cta.3312) [IF=2.038 (2020)]
53)	Jugal kishore Bhandari, Yogesh Kumar Verma, Laxman Singh and Santosh Kumar Gupta, <i>“A Novel Design of High Performance Hybrid Multiplier”</i> , <i>Journal of Circuits, Systems and Computers</i> , Accepted: 09 May 2022, World Scientific, (https://doi.org/10.1142/S0218126622502681) [IF=1.333 (2020)]
54)	Manoj Kumar Yadav, Santosh Kumar Gupta, <i>“First Principle Study of Spin Tunneling Current under Field Effect in Magnetic Tunnel Junction for Possible Application in STT-RAM”</i> , <i>IEEE Transactions on Electron Devices</i> , Accepted on 07 July 2022. (https://doi.org/10.1109/TED.2022.3190251) [IF=3.221 (2021)]
55)	Devarshi Shukla, Santosh Kumar Gupta, Vijaya Bhadauria, Rajeev Tripathi, <i>“High gain, low noise, low voltage, and low power current mode up-conversion mixer for 5G Application”</i> , <i>IETE Journal of Research (TIJR)</i> , Accepted: 15 July 2022 (https://doi.org/10.1080/03772063.2022.2103039)

[IF=2.333 (2020)]

BOOKS/CHAPTES

1)	Yogesh Kumar Verma and Santosh Kumar Gupta, Advanced VLSI Design and Testability , Chapter: Performance Analysis of AlGa _N /Ga _N Heterostructure Field Effect Transistor (HFET), ISBN: 9781003083436, CRC Press, Taylor and Francis, 2021
2)	Yogesh Kumar Verma, Varun Mishra, Lucky Agarwal, Laxman Singh, and Santosh Kumar Gupta, HEMT Technology and Applications , Chapter 4: Study of Different Transport Properties of MgZnO/ZnO and AlGa _N /Ga _N High Electron Mobility Transistors: A Review, ISBN: 978-981-19-2164-3, Springer, 2022, pp. 53-70 (https://doi.org/10.1007/978-981-19-2165-0)
3)	Yogesh Kumar Verma, Varun Mishra, Rajan Singh, Trupti Ranjan Lenka, and Santosh Kumar Gupta Chapter 15: Linearity Analysis of AlN/ β -Ga ₂ O ₃ HEMT for RFIC Design, ISBN: 978-981-19-2164-3, Springer, 2022, pp. 221-232, (https://doi.org/10.1007/978-981-19-2165-0)

INTERNATIONAL CONFERENCES

1)	Pavan K. Manchi, S. K. Gupta, " Corner Effects In Soi Vs Bulk Tri-Gate FINFETs Using 3D Device Simulation ", <i>International Conference on Industrial Engineering and Operations management (IEOM)</i> , Dhaka, Bangladesh, 9-10 January 2010,
2)	S. K. Gupta, Kaushik Guha, S. Baishya, " A Two Dimensional Surface Potential model of Double Material Double Gate Surround Gate (DMDG-SG) MOSFETs ", <i>International Conference on Artificial Intelligence and Machine Learning (AIML)</i> , Dubai, United Arab Emirates, pp.143-147, 12-14 April, 2011,
3)	S. K. Gupta, Gaurab Gunjan Pathak, Debajit Das, Chandan Sharma, " Design and Simulation of a Two Stage OPAMP Using DG MOSFETs for Low Power and Low Voltage Applications ", <i>International Conference on Wisdom Computing and Communication (WCAC 2011)</i> , Kuala Lumpur, Malaysia, 17-18 November, 2011,
4)	Varun Mishra, Yogesh Kumar Verma, Prateek Kishor Verma, and Santosh Kumar Gupta, " Ferroelectric Dual Material Gate All Around TFET Architecture for Enhanced Electrical Performance ", <i>15th IEEE India Council International Conference (INDICON)</i> , Coimbatore, INDIA, pp.1-4, 12/2018, Published By IEEE.
5)	Prateek Kishor Verma, Akash Singh Rawat, Santosh Kumar Gupta, " Temperature Dependent Analog, RF and Linearity Analysis of Junctionless Quadruple Gate MOSFETs for Analog Applications ", <i>1st International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, 11/2018, Published By Springer.
6)	Digvijay Singh Mehta, Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, " A Hardware Minimized Gated Clock Multiple Output Low Power Linear Feedback Shift Register ", <i>1st International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, 11/2018, Published By Springer.

7)	Prateek Kishor Verma, Varun Mishra, Yogesh Kumar Verma, Pawan Kumar Yadav, Santosh Kumar Gupta, "A Novel Dual Material Fin Field Effect Transistor for High Performance Nanoscale Applications" , <i>1st International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, 11/2018, Published By Springer.
8)	Varun Mishra, Yogesh Kumar Verma, Prateek Kishor Verma, Ningthoujam Qoonand Singh, Santosh Kumar Gupta, "Performance of Double Gate Tunnel FET Devices with Source Pocket" , <i>1st International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, 11/2018, Published By Springer.
9)	Shalini Mishra, Devarshi Shukla, Vijaya Bhadauria, Santosh Kumar Gupta, "Tuned Universal Filter Design using single Differential Difference Current Conveyor for sub-GHz frequency Band" , <i>1st International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, 11/2018, Published By Springer.
10)	Yogesh Kumar Verma, Varun Mishra, Prateek Kishor Verma, Santosh Kumar Gupta, and Rajeev Kumar Chauhan, "Effect of Ionizing Radiation and Temperature on SiGe HBT" , <i>5th IEEE Uttar Pradesh Section International Conference on Electrical, Electronics, and Computer Engineering (UPCON)</i> , Madan Mohan Malaviya University of Technology, Gorakhpur, INDIA, pp.1-6, 11/2018, Published By IEEE.
11)	Yogesh Kumar Verma, Varun Mishra, Prateek Kishor Verma, Santosh Kumar Gupta, Rajeev Kumar Chauhan, "Impact of Extrinsic Reliability Issues including Radiation and Temperature on SiGe HBT" , <i>Computational and Characterization Techniques in Engineering and Sciences (CCTES)</i> , Integral University, Lucknow, INDIA, 09/2018,
12)	Yogesh Kumar Verma, Santosh Kumar Gupta, Varun Mishra, Rajeev Kumar Chauhan, "Reliability Analysis of SiGe Heterojunction Bipolar Transistor" , <i>IEEE Sponsored Third International Conference for Convergence of Technology, (I2CT)</i> , Pune, 04/2018, Published By IEEE.
13)	Yogesh Kumar Verma, Santosh Kumar Gupta, Varun Mishra, Prateek Kishor Verma, "Surface Potential based analysis of MgZnO/ZnO High Electron Mobility Transistors" , <i>IEEE International Students' Conference on Electrical, Electronics and Computer Sciences(SCEECS)</i> , MANIT Bhopal 2018, 02/2018, Published By IEEE.
14)	Varun Mishra, Santosh Kumar Gupta, Yogesh Kumar Verma, Vishal Ramola and Abhishek Bora, "A High-Gain, Low-Power Latch Comparator Design for Oversampled ADCs" , <i>Fifth International Conference on Signal Processing and Integrated Networks, (SPIN 2018)</i> , Amity University, 02/2018, Published By IEEE.
15)	Santosh Kumar Gupta, Sangaraju Varun, "Slotted I-Patch with Capacitive Probe Fed Microstrip Antenna for Wideband Applications" , <i>2nd International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.
16)	Prateek Kishor Verma, Santosh Kumar Gupta, "Virtually Doped Silicon-on-Insulator Junctionless Transistor for Reduced OFF state Leakage Current" , <i>2nd International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.
17)	Sanjay Kumar Prajapati, Devarshi Shukla, Santosh Kumar Gupta, "Improved Noise Margin and Reduced Power Consumption in Subthreshold Adiabatic Logic using Dual Rail Power Supply" , <i>2nd International Conference on VLSI, Communication and Signal Processing</i> , MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.
18)	Abhishek Kumar, Santosh Kumar Gupta, Vijaya Bhadauria, "A Low Power Approach for

	<i>Designing 12-bit current steering DAC", 2nd International Conference on VLSI, Communication and Signal Processing, MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.</i>
19)	Mamidi Nagaraju, Santosh Kumar Gupta, Vijaya Bhadauria, Devarshi Shukla, "An Efficient Mixed Parallel Pipeline SAD Architecture Design and Implementation for HEVC Motion Estimation" , 2nd International Conference on VLSI, Communication and Signal Processing, MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.
20)	Abhishek Kumar, Sushmita Jaiswal, Santosh Kumar Gupta, "Controlling GIDL using Core-Shell Technique in Conventional Nanowire" , 2nd International Conference on VLSI, Communication and Signal Processing, MNNIT Allahabad, Prayagraj, INDIA, 10/2019, Published By Springer.
21)	Devarshi Shukla, Abhishek Kumar, Vijaya Bhadauria, Santosh Kumar Gupta, "865-867 MHz 180 nm Transmitter with Direct BPSK Modulation for Wireless Sensor Application" , 3rd International Conference on VLSI, Communication and Signal Processing (VCAS 2020), Motilal Nehru National Institute of Technology Allahabad, Prayagraj-211004, India, 10/2020, Published By Springer.
22)	Varun Mishra, Yogesh Kumar Verma, Santosh Kumar Gupta, Afreen Anamul Haque, "Charge Plasma Based Tunnel FET with Enhanced DC Performance Applicable for Ultra Low Power Applications" , 3rd International Conference on VLSI, Communication and Signal Processing (VCAS 2020), Motilal Nehru National Institute of Technology Allahabad, Prayagraj-211004, India, 10/2020, Published By Springer.
23)	Prateek Kishor Verma, Santosh Kumar Gupta, "Realizing More Efficient Volume Depletion in Virtually Doped High-k BOX Junctionless Transistors" , 3rd International Conference on VLSI, Communication and Signal Processing (VCAS 2020), Motilal Nehru National Institute of Technology Allahabad, Prayagraj-211004, India, 10/2020, Published By Springer.
24)	Yogesh Kumar Verma, Santosh Kumar Gupta, Rajeev Kumar Chauhan, Prateek Kishor Verma, "Effect of Temperature on Linearity and Distortion Parameters of Gate All Around AlGaN/GaN High Electron Mobility Transistor" , International Conference on Communication Computing and Signal Processing, NIT Jalandhar, Punjab, INDIA, 07/2020,
25)	Yogesh Kumar Verma, Laxman Singh, Varun Mishra, Rohit Gurjar, Prateek Kishor Verma, Santosh Kumar Gupta and Manoj Singh Adhikari, "Oxide Thickness Variation Effects in MOS AlGaN/GaN HFET" , 3rd International Conference on Intelligent Circuits and Systems, Lovely Professional University, Punjab, INDIA, 06/2020, Published By CRC Press.
26)	Mishra V.;Verma Y.K.;Gupta S.K.;Haque A.A., "Charge Plasma-Based Tunnel FET with Enhanced DC Performance Applicable for Ultra-low Power Applications" , Lecture Notes in Electrical Engineering, Volume 777, Year 2022, Pages 1137-1147. 4 th International Conference on VLSI, Communication and Signal Processing (VCAS 2021), Motilal Nehru National Institute of Technology Allahabad, Prayagraj-211004, India, 09/2021, Published By Springer
27)	Shukla D.;Kumar A.;Bhaduaria V.;Gupta S.K, "865–867 MHz 180 nm Transmitter with Direct BPSK Modulation for Wireless Sensor Application" , Lecture Notes in Electrical Engineering, Volume 777, Year 2022, Pages 669-682. 4 th International Conference on VLSI, Communication and Signal Processing (VCAS 2021), Motilal Nehru National Institute of Technology Allahabad, Prayagraj-211004, India, 09/2021, Published By Springer

NATIONAL CONFERENCES

1)	S. K. Gupta, S. Baishya, " 3D-TCAD Simulation Study of an Electrically Induced Source/Drain Cylindrically Surrounding Gate MOSFETs for reduced SCEs and HCEs ", <i>IEEE International Conference on Electronics and Computer Technology (ICECT) 2011</i> , Kanyakumari, INDIA, pp.429-432, 8-10 April, 2011, Published By IEEE.
2)	S. K. Gupta, GaurabGunjanPathak, Debajit Das, Chandan Sharma, " Double Gate MOSFET And Its Application For Efficient Digital Circuits ", <i>IEEE International Conference on Electronics and Computer Technology (ICECT) 2011</i> , Kanyakumari, INDIA, pp.33-36, 8-10 April, 2011, Published By IEEE.
3)	S. K. Gupta, S. Baishya, " Analog and RF Performance Evaluation of a Novel Junctionless Triple Metal Cylindrical Surround Gate (JLTM CSG) MOSFET ", <i>IEEE INDIA Conference (INDICON-2013)</i> , IIT Bombay, INDIA, pp.1-4, 13-15 December 2013, Published By IEEE.
4)	S. K. Gupta, S. Baishya, " Effect of high-k gate materials on the analog and RF performance of double metal double gate (DMDG) MOSFETs ", <i>IEEE INDIA Conference (INDICON- 2013)</i> , IIT Bombay, INDIA, pp.1-6, 13-15 December, 2013, Published By IEEE.
5)	S. K. Gupta, Achinta Baidya, S. Baishya, " Simulation and Optimization of Lightly-Doped Ultra-Thin Triple Metal Double Gate (TM-DG) MOSFET with High-K Dielectric for Diminished Short Channel Effects ", <i>IEEE International Conference on Computer and Communication Technology (ICCCT 2011)</i> , MNNIT Allahabad, INDIA, pp.221-224, 15-17 September 2011, Published By IEEE.
6)	Pavan K. Manchi, S. K. Gupta, " Corner Effects In SOI Tri-Gate FINFET Structure By Using 3D Process And Device Simulations ", <i>IEEE International Conference on Computer and Communication Technology (ICCCT 2010)</i> , MNNIT Allahabad, INDIA, pp.683-686, 17-19 September 2010, Published By IEEE.
7)	S. K. Gupta, AbhashGarg, NitinPrabhakar Singh, " Design And Simulation Of An Improved Dual Band LNA For WLAN Applications ", <i>IEEE International Conference on Computer and Communication Technology (ICCCT 2010)</i> , MNNIT Allahabad, INDIA, pp.678-682, 17-19 September 2010, Published By IEEE.